

FIG. 1

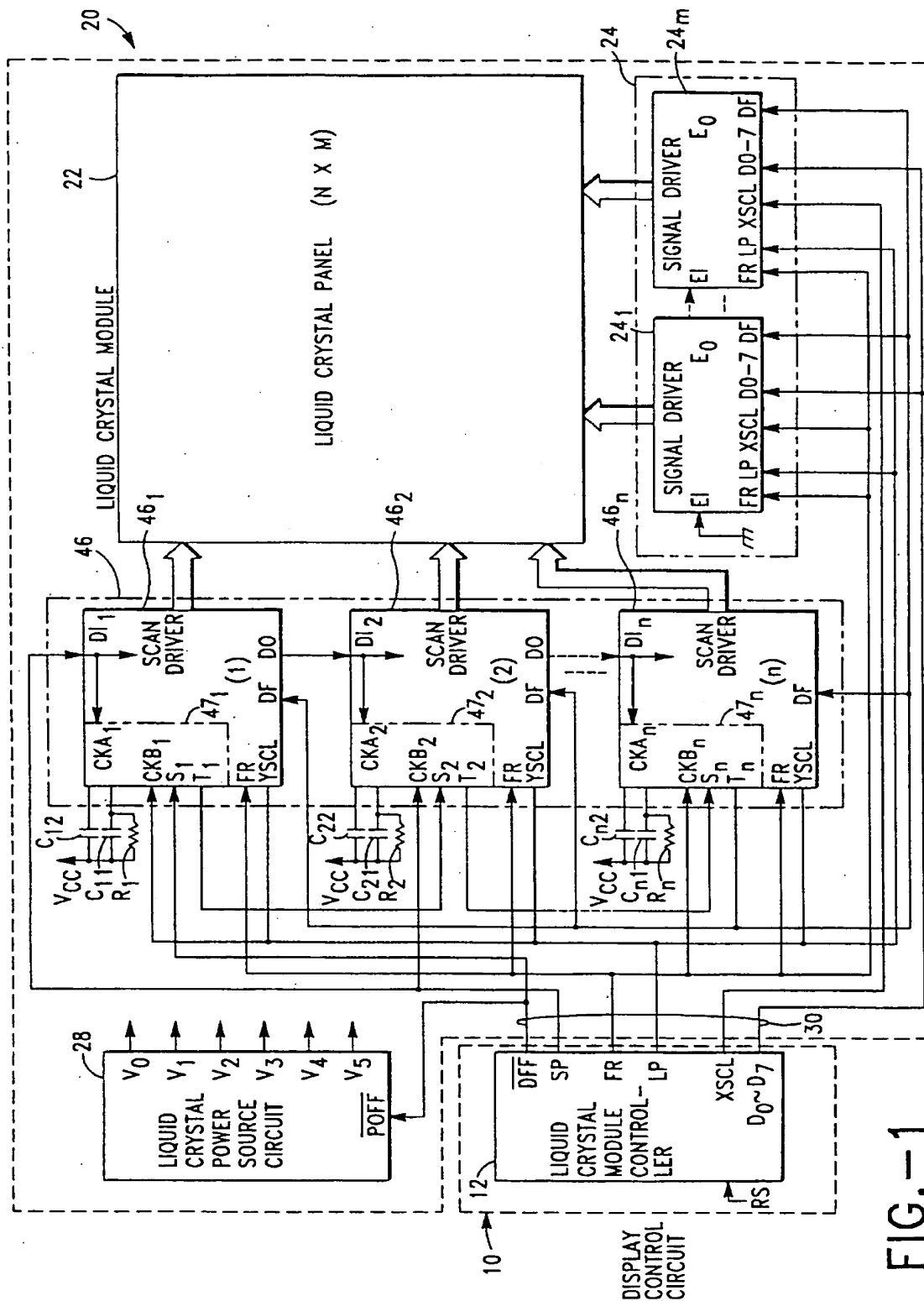


FIG.-1

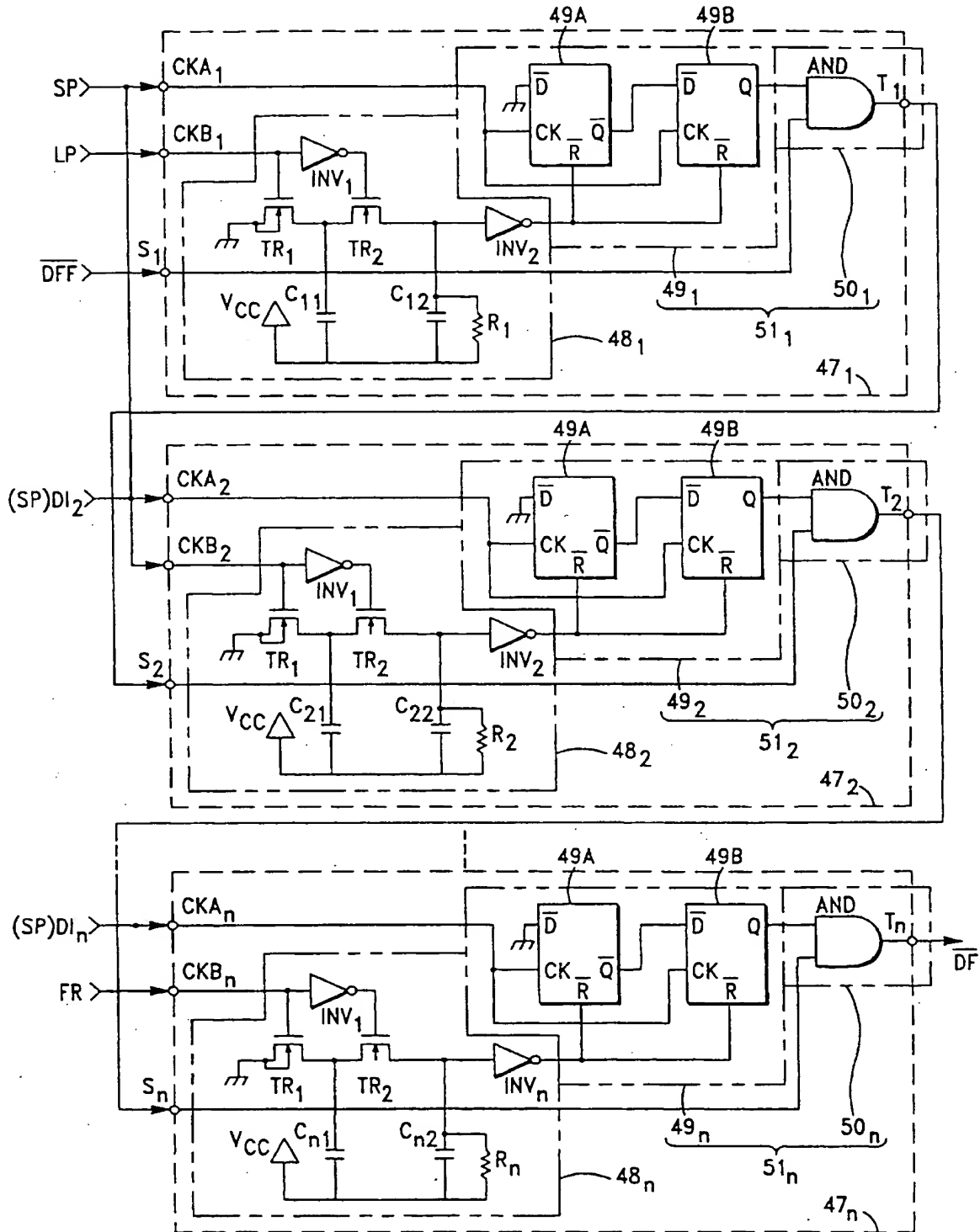


FIG.-2

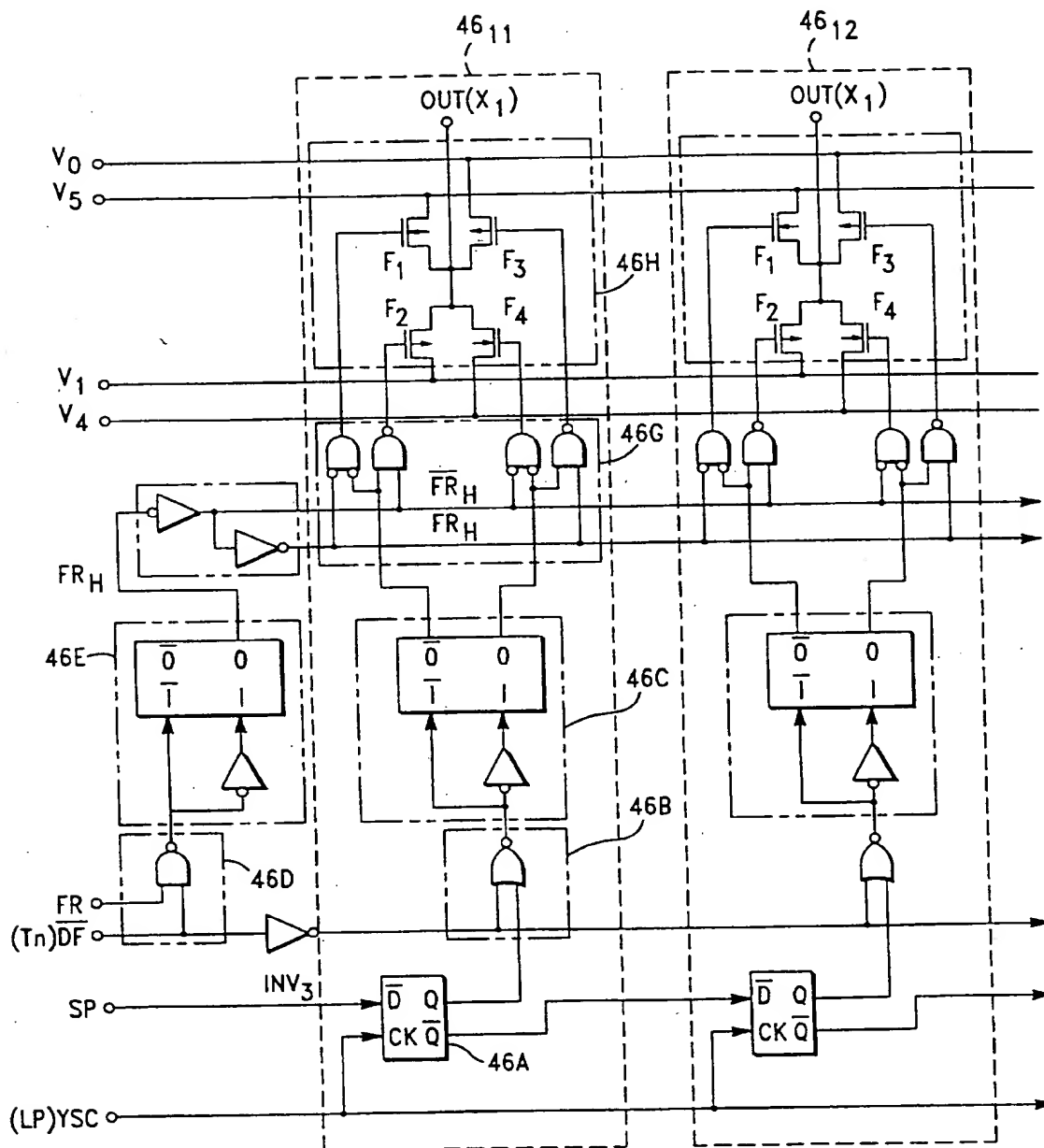
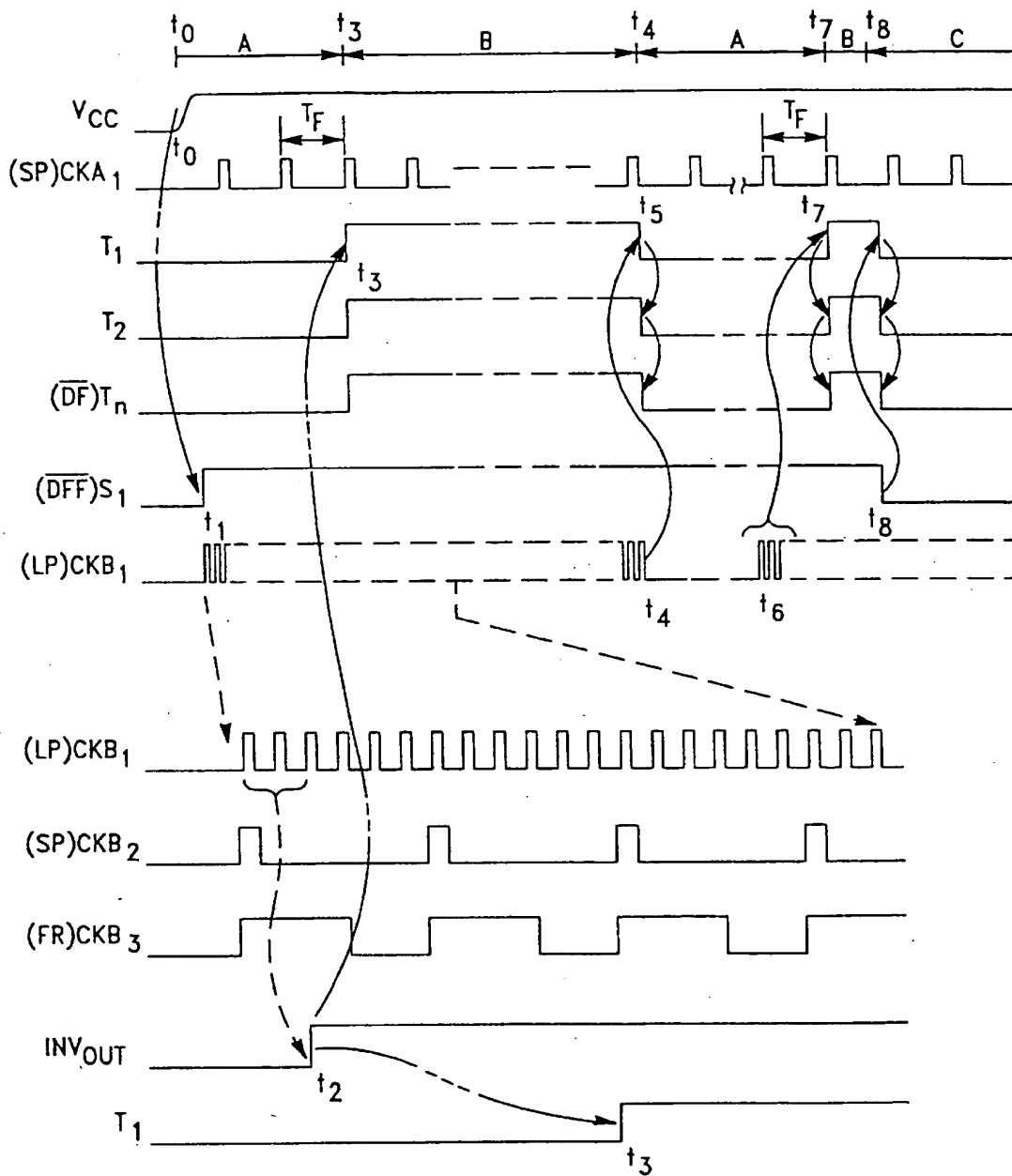


FIG.-3



A: LIQUID CRYSTAL DRIVE INHIBIT PERIOD
 B: LIQUID CRYSTAL DRIVE PERIOD
 C: DISPLAY-OFF PERIOD

FIG.-4

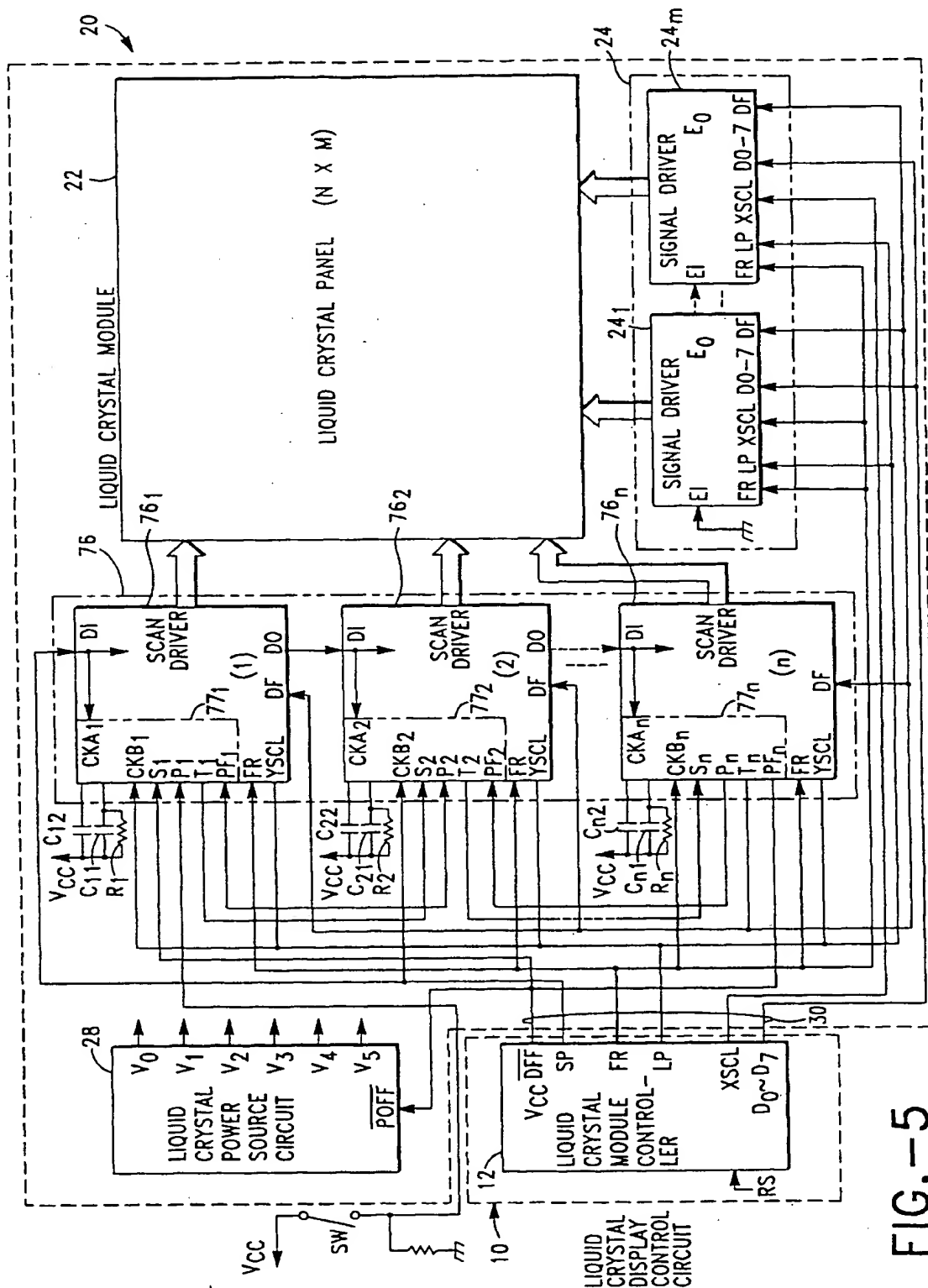
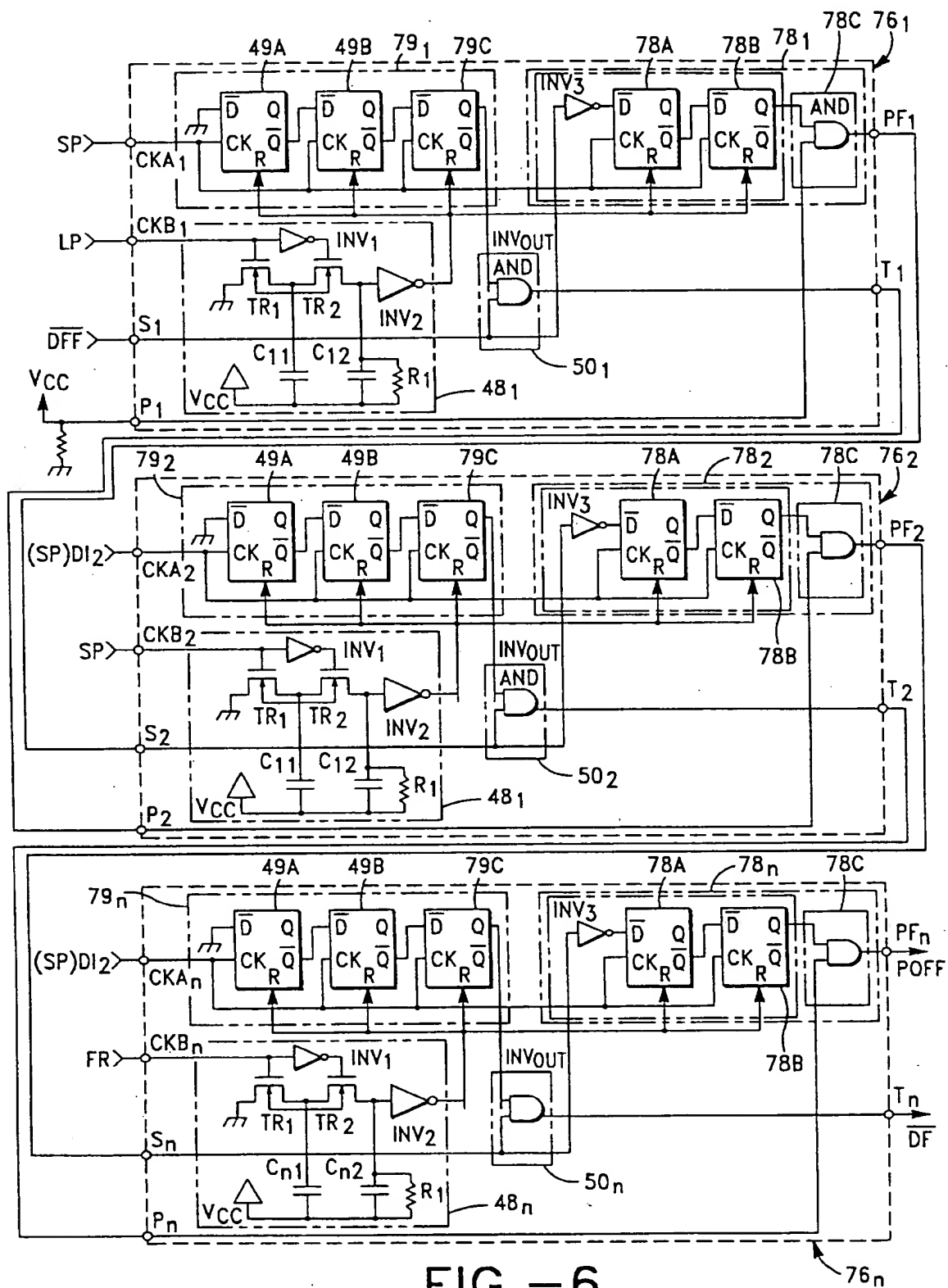


FIG. 5 is a block diagram of the LCD system.



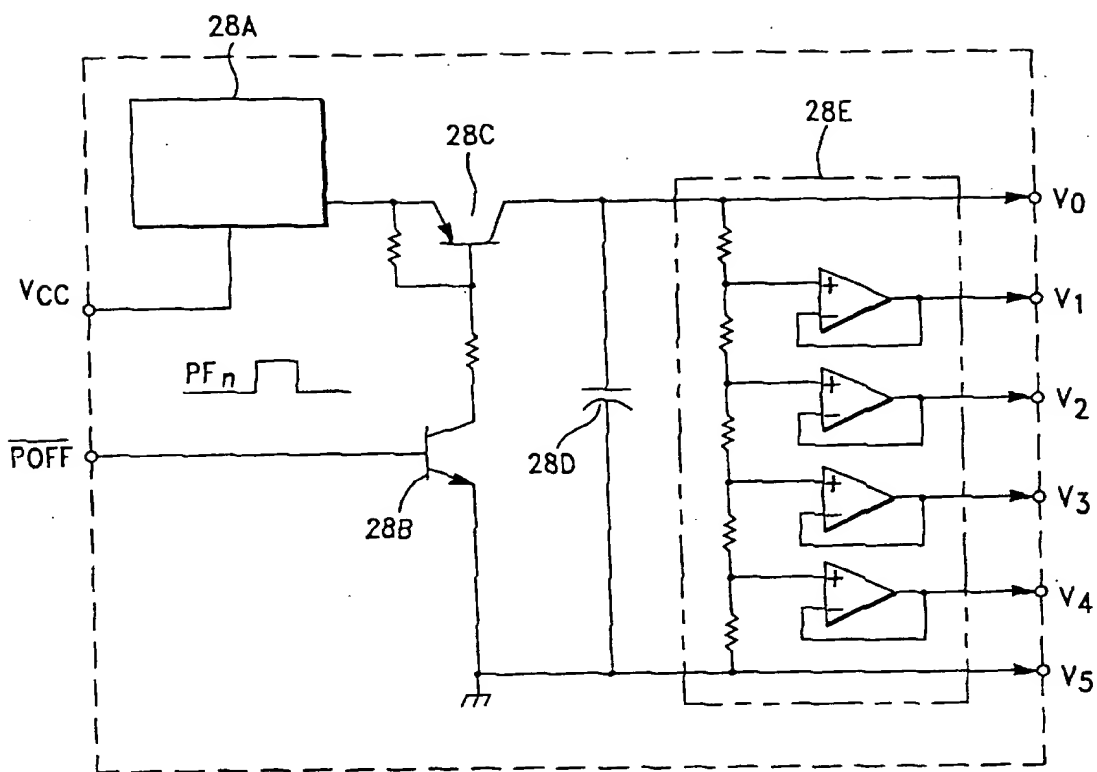
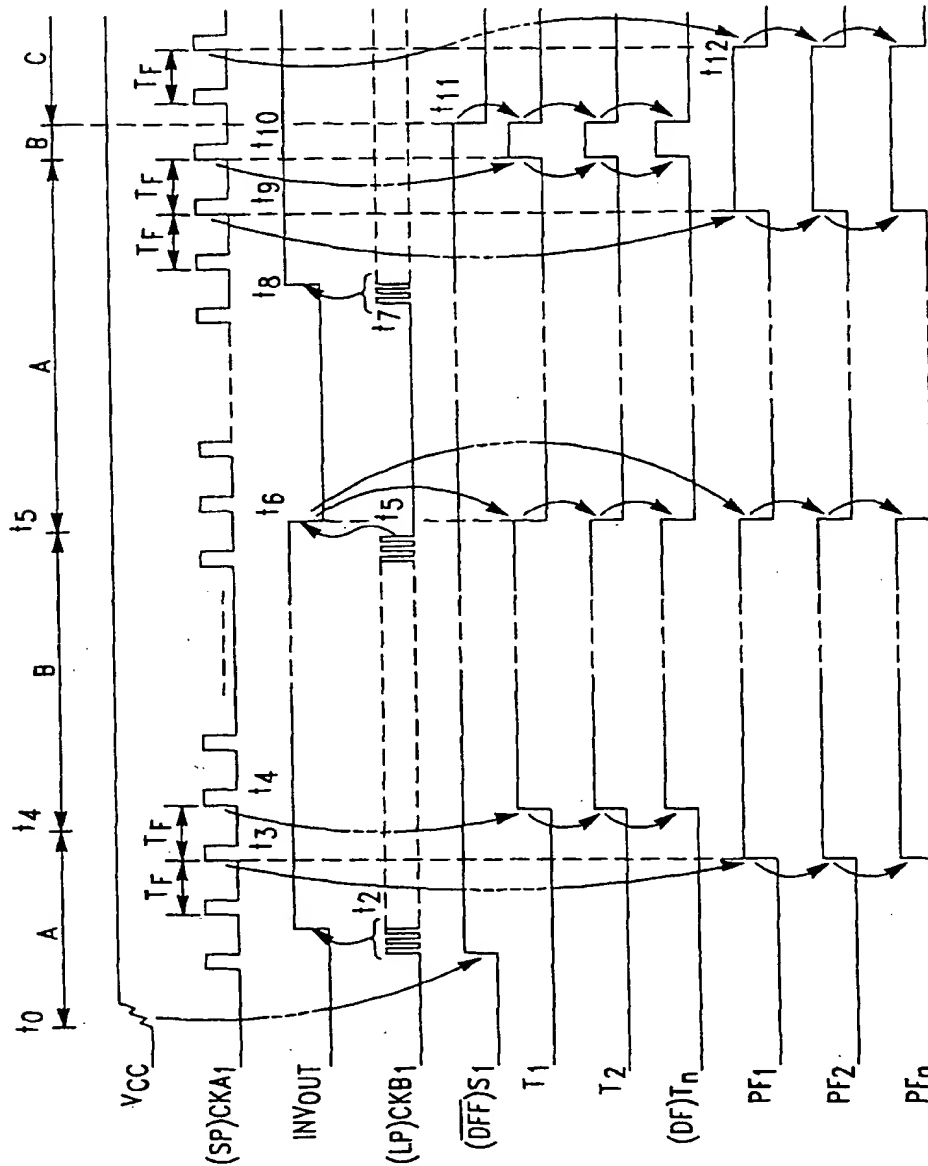


FIG.-7
(PRIOR ART)



A: LIQUID CRYSTAL DRIVE INHIBIT CIRCUIT
B: LIQUID CRYSTAL DRIVE PERIOD
C: DISPLAY-OFF PERIOD

FIG.-8

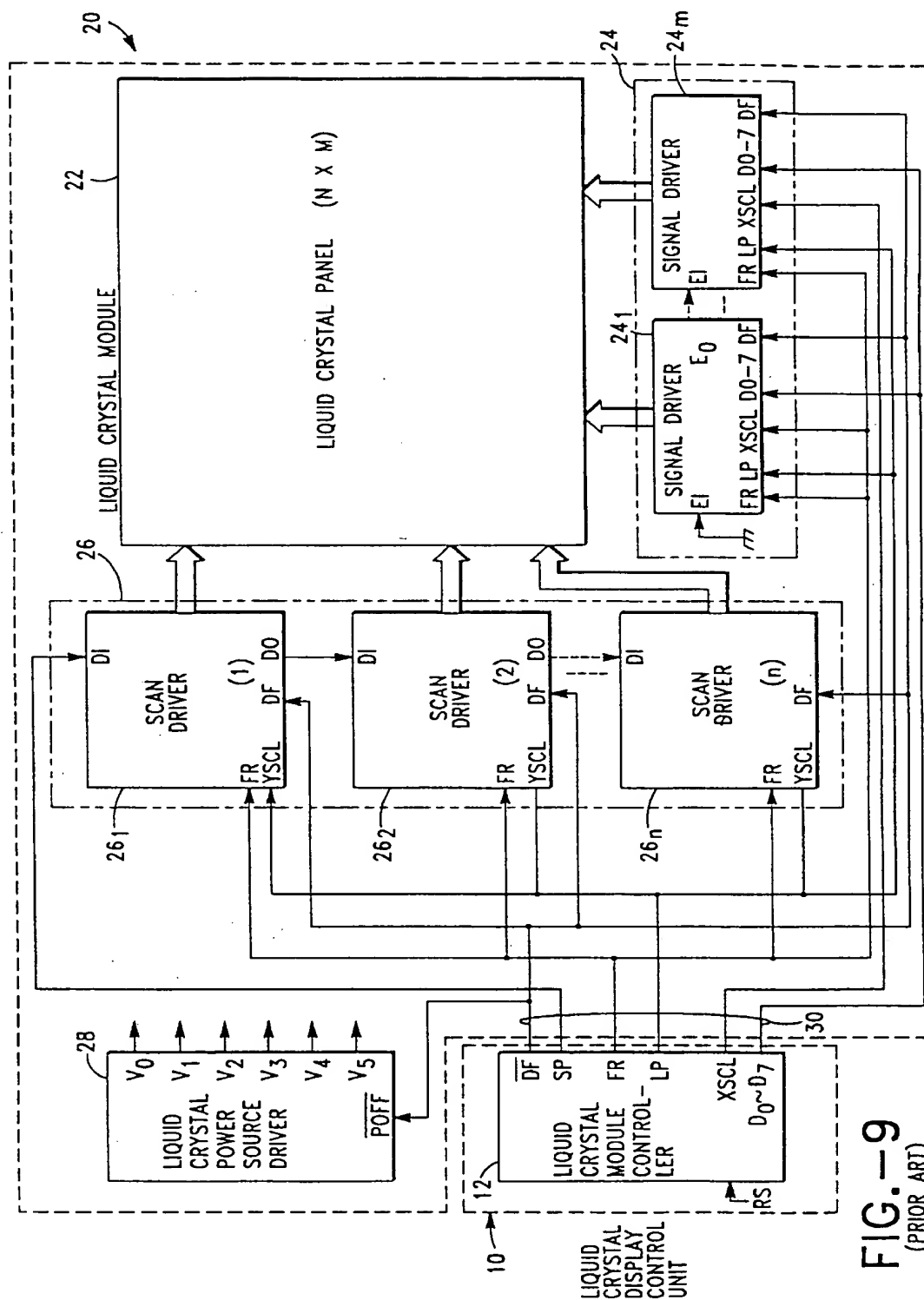


FIG. -9
(PRIOR ART)